

TX38D14VCOCAA (15.1" Hitachi TFT LCD)

DESCRIPTION

The specifications are applied to the following SUPER TFT-LCD Modules.

Note : Inverter device for Back-Light is not built in the module.

Type Name: TX38D14VCOCAA

General Specifications

Effective Display Area	H 304.128 × V 228.096	(mm)
Resolution	XGA	
Number of Pixels	H 1024 × V 768	(pixels)
Pixel Pitch	H 0.297 × V 0.297	(mm)
Color Pixel Arrangement	R·G·B Vertical Stripe	
Display Mode	Transmissive Mode, Normally Black Mode	
Top polarizer Type	Anti-glare	
Number of Colors	16,777,216	(colors)
Viewing Angle Range	Super Wide version (Horizontal & Vertical : 160° at each direction, on the condition of Contrast Ratio ≥ 20)	
Input Signal	LVDS 1-channel	
Back-Light	4 pcs. of CFL (Edge Light 2 pcs. at each top and bottom side)	
External Dimensions	H 350 × V 265 × t 19	(mm)
Weight	Max. 1700	(g)

Note LVDS : Low Voltage Differential Signaling
CFL : Cathode Fluorescent Lamp



5. INTERFACE PIN CONNECTION

5.1 TFT-LCD MODULE

CN1 : FI-SE20P-HF (JAE) Mating connector : FI-SE20M (JAE)

Pin No.	Symbol	Description	Note
1	VDD	Power Supply (typ.+5V)	1)
2	VDD		
3	VSS	GND (0V)	2)
4	VSS		
5	Rx0-	Pixel Data	3)
6	Rx0+		
7	VSS	GND (0V)	2)
8	Rx1-	Pixel Data	3)
9	Rx1+		
10	VSS	GND (0V)	2)
11	Rx2-	Pixel Data	3)
12	Rx2+		
13	VSS	GND (0V)	2)
14	CLK-	Pixel Clock	3)
15	CLK+		
16	VSS	GND (0V)	2)
17	Rx3-	Pixel Data	3)
18	Rx3+		
19	VSS	GND (0V)	2)
20	VDD	Power Supply (typ.+5V)	1)

- Notes
- 1) All VDD pins shall be connected to +5.0V(Typ.).
 - 2) All VSS pins shall be grounded. Metal bezel is internally connected to VSS.
 - 3) Rx n+ and Rx n- (n=1,2,3) should be wired by twist-pairs or side-by-side FPC patterns, respectively.

5.2 BACK-LIGHT UNIT

CN2, CN3 : BHR-04VS-1 (J.S.T.)

Mating connector: SM03(7-D1)B-BHS-1 (J.S.T.)

Pin No.	Symbol	Description	Note
1	VL	Power Supply	
2	VL	Power Supply	
3	NC	No connection	
4	GND	GND(0V)	

